



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4352

SERIAL NUMBER 10/664,469	FILING DATE 09/17/2003  RULE	CLASS 365	GROUP ART UNIT 2827	ATTORNEY DOCKET NO. NSC1-M3500 [P05681]
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Yuri Mirgorodski, Sunnyvale, CA;  
 Vladislav Vashchenko, Palo Alto, CA;  
 Peter J. Hopper, San Jose, CA;

\*\* CONTINUING DATA \*\*\*\*\* *None* *DL*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *None* *DL*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 12/08/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>DL</i> Examiner's Signature Initials	STATE OR COUNTRY CA	SHEETS DRAWING 3	TOTAL CLAIMS 7	INDEPENDENT CLAIMS 1
--	--	---------------------------	------------------------	----------------------	----------------------------

ADDRESS  
 28584  
 STALLMAN & POLLOCK LLP  
 SUITE 2200  
 353 SACRAMENTO STREET  
 SAN FRANCISCO , CA  
 94111

TITLE  
 Method of PMOS stacked-gate memory cell programming enhancement utilizing stair-like pulses of control gate voltage

FILING FEE  RECEIVED 880	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
-----------------------------------	---	--